

FIG. 1

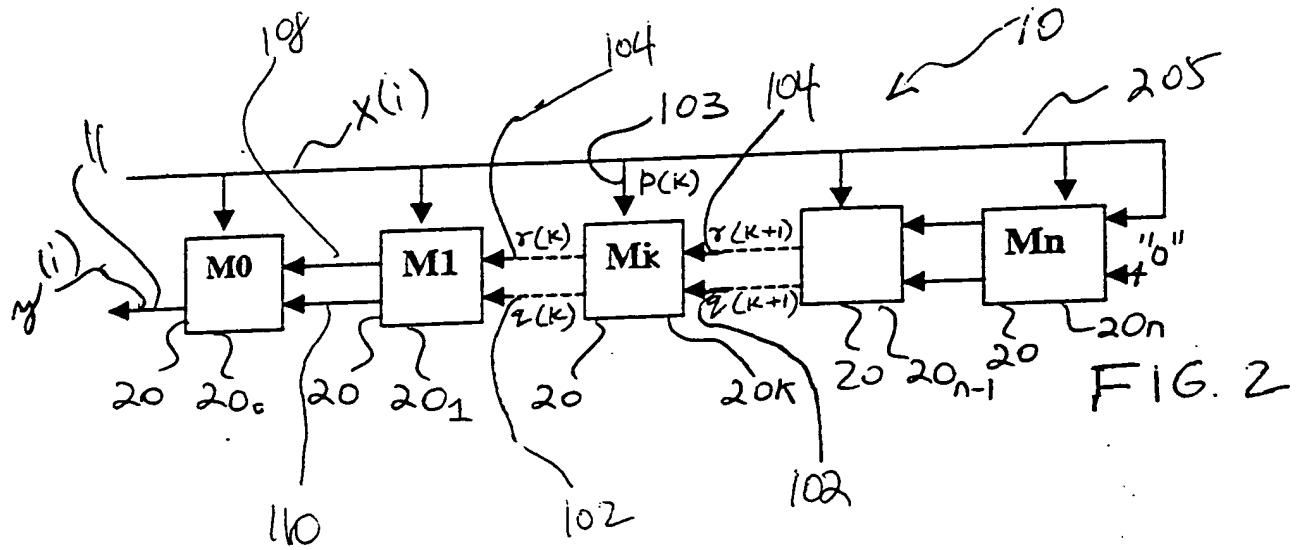


FIG. 2

FIG. 3 is a block diagram of a digital filter. The diagram shows a sequence of operations within a dashed boundary. Inputs include  $r(k)$  (108),  $p(k)$  (24), and  $q(k)$  (110). The output is  $r(k+1)$  (104). The internal components include two registers (22, 20k), an adder (32), a multiplier (34), and another adder (36). A coefficient (30) is also shown. The diagram illustrates the flow of data and the calculation of  $q(k+1)$  (102).

